UTILITY PATENT APPLICATION **TRANSMITTAL**

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 243575US2

First Inventor or Application Identifier Takuya FUTATSUYAMA, et al.

NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE, METHOD FOR SUB-BLOCK ERASE AND ELECTRIC DEVICE WITH THE SAME

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See MPEP	APPLICATION ELEI chapter 600 concerning utility p		ADDRE	SS TO:	Commissioner for P Mail Stop Patent Ap Alexandria, Virginia	plication	U.S. F 37817	
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2. Spec	ification 1	Total Sheets 35		Application	on Data Sheet. See	37 CFR 1.76	<i>,</i>	
			9. 🗆	(when there	. §3.73(b) Statement is an assignee)	☐ Attorney		
3. Draw	ing(s) <i>(35 U.S.C. 113)</i>	Total Sheets 12		_	ranslation Document on Disclosure	: (if applicable) ☐ Copies of	f IDS	
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Prior application information: Examiner: Group Art Unit:								
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.								
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C. Irvin McClelland Registration Number 21,124

Docket No. 243575US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Takuya FUTATSUYAMA, et al.

SERIAL NO: New Application

FILING DATE: Herewith

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

FOR: NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE, METHOD FOR SUB-BLOCK ERASE

AND ELECTRIC DEVICE WITH THE SAME

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	•	Marvin J. Spivak
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